Fig. 1A

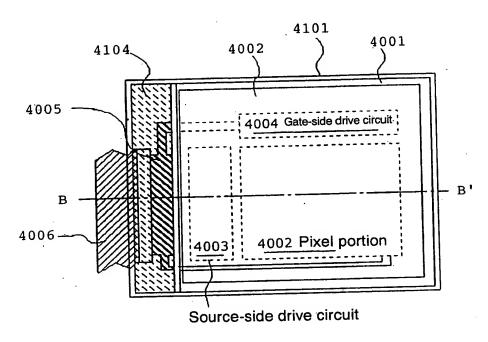
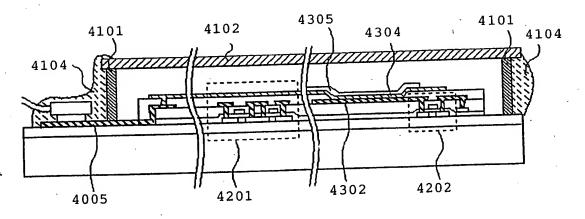


Fig. 1B



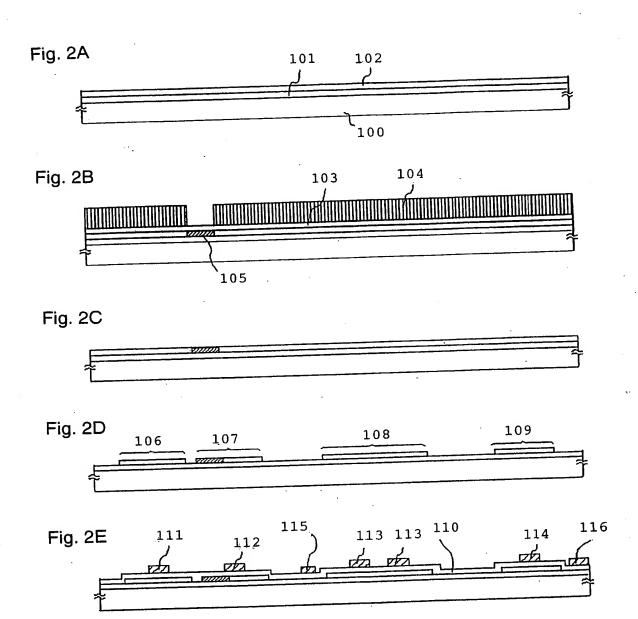
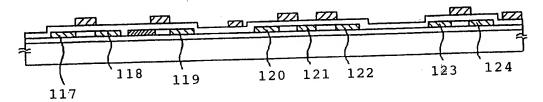
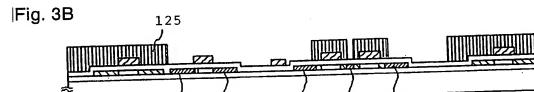


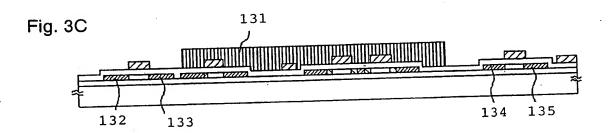
Fig. 3A





127

126



128

129

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Fig. 3D

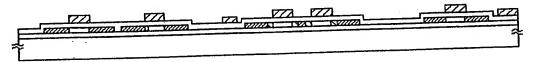
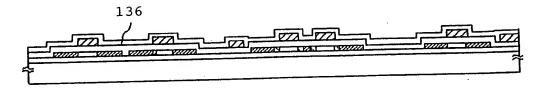
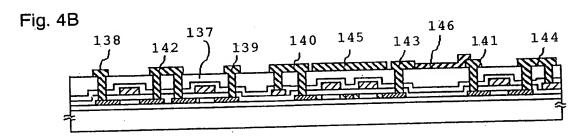


Fig. 4A





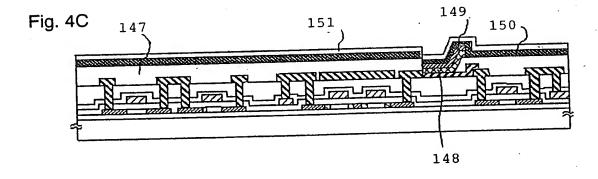


Fig. 5

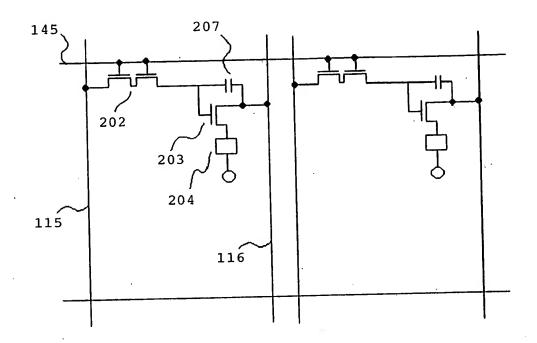
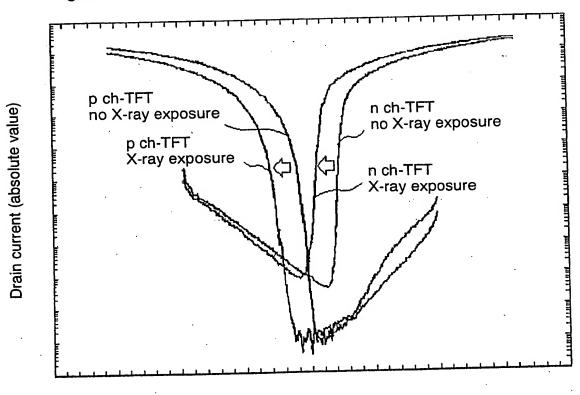


Fig. 6



Gate voltage

Fig. 7A

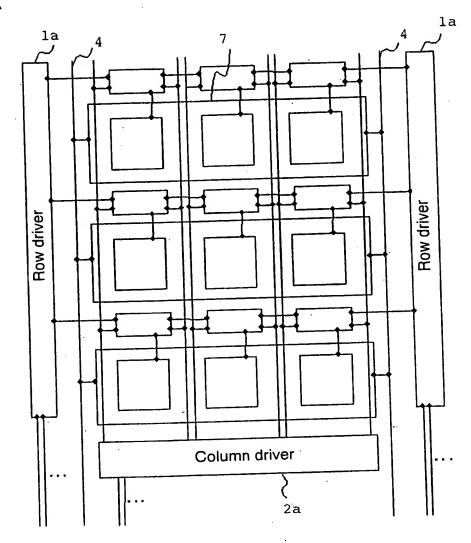


Fig. 7B

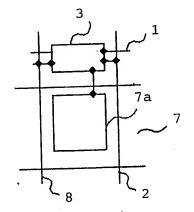


Fig. 8

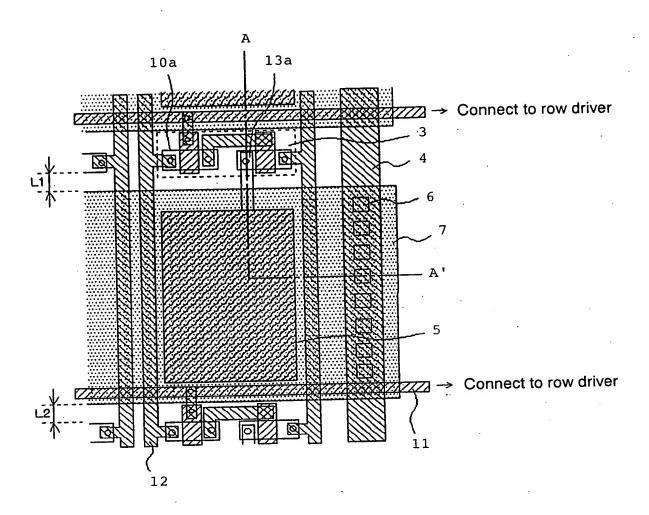


Fig. 9A

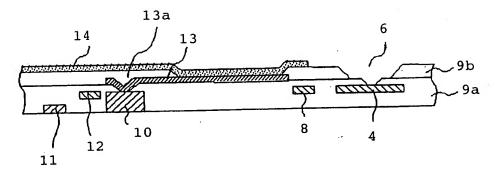


Fig. 9B

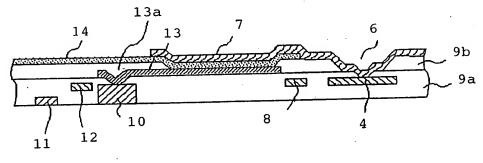


Fig. 10

